## IN THE CLAIMS

Please cancel Claim 10 without prejudice and amend Claims 6 and 9, and 10 as follows:

- 1. (Original) A front end for a high frequency receiver (1), which front end comprises a low noise amplifier (2), characterised in that the low noise amplifier is a quadrature low noise amplifier (2-1, 2-2).
- 2. (Original) A high frequency receiver (1), which is provided with a front end comprising a low noise amplifier (2), and which is provided with quadrature mixers (3) coupled to the low noise amplifier (2), characterised in that the low noise amplifier is a quadrature low noise amplifier (2-1, 2-2).
- 3. (Original) The high frequency receiver (1) according to claim 2, characterised in that quadrature paths (I, Q) of the quadrature low noise amplifier (2-1, 2-2) are implemented differentially.
- 4. (Original) The high frequency receiver (1) according to claim 3, characterised in that the differential quadrature low noise amplifier (2-1; 2-2) is constructed as a class AB operating circuit.

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- 5. (Previously Presented) The high frequency receiver (1) according to claim 2, characterised in that the quadrature low noise amplifier (2-1, 2-2) comprises a cascode arrangement of semiconductors (15).
- 6. (Currently Amended) The high frequency receiver (1) according to claim 5, characterised in that the semiconductors (15) are of the type MOST, such as NMOST or PMOST, or FET, such as MESFET, or the like.
- 7. (Previously Presented) The high frequency receiver (1) according to claim 5, characterised in that across the cascode arrangement of semiconductors (15) there is connected a capacitor (C).
- 8. (Previously Presented) The high frequency receiver (1) according to claim 2, characterised in that the high frequency receiver (1) comprises two quadrature choppers (10-1, 10-2) coupled between respective outputs (4, 5) of the quadrature low noise amplifiers (2-1, 2-2) and respective inputs of the quadrature mixers (3-1, 3-2).

## 10. (Cancelled)

11. (Previously Presented) A quadrature low noise amplifier (2-1, 2-2) for application in the high frequency receiver (1) according to claim 2.